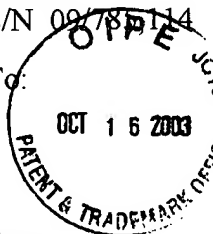


41
2823
Docket: T00-338C

S/N 09/785,114

To:



Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

From:

George O. Saile, Reg. No. 19,572
28 Davis Ave
Poughkeepsie, NY 12603

RECEIVED
OCT 21 2003
TECHNOLOGY CENTER 2800

Subject:

Docket T00-338

S/N 09/785,114

File date: 2/20/2001

Inventor: Chin

Title : **A LOW LEAKAGE ONE TRANSISTOR STATIC
RANDOM ACCESS MEMORY**

Group Art Unit: 2823

Examiner: Berezny

RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the office action dated 7/11/03, please consider the following
remarks:

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patent and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, on October 14, 2003.

Signature/Date

Stephen B. Ackerman, Reg NO. 37,761

Docket: T00-338C
S/N 09/785,114

This response to Office Action has the following sections:

- **Amendments to the Claims** are reflected in the listing of claims that begin on the next page
- **Remarks/Arguments** begin on the following page after the above section.